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32. The device according to Claim 18, wherein said programming control circuits are arranged adjacent to said memory cell array.

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33. The device according to Claim 18, wherein said programming control circuits include bit line voltage regulators for selectively changing voltages of said bit lines according to said first and second data.

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34. The device according to Claim 33, wherein said voltages of said bit lines are selectively and simultaneously changed by said bit line voltage regulators.--

REMARKS

Favorable consideration of this application, as presently amended, is respectfully requested.

The present application is a Divisional Application of copending parent U.S. Application Serial No. 08/308,534.

In this parent application, in an Official Action of May 16, 1995, an election of species requirement was set forth dividing the parent invention into four species. The parent of the present application elected one specific group of species and the claims directed thereto.

The present divisional application is now being submitted to pursue one of the further elected species.

By the present response, new Claims 18-34 have been submitted for examination which correspond to a further species.

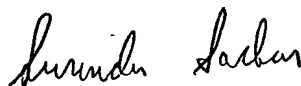
Furthermore, by the present response the specification has been amended to correct for minor grammatical and

idiomatic informalities. These changes made to the specification are the same changes to the specification which were presented and approved in the parent of the present application.

In view of the present preliminary amendment, it is believed that the present application is now in condition for a full and thorough examination on the merits. An early and favorable consideration of the present application is hereby respectfully requested.

Respectfully submitted,

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